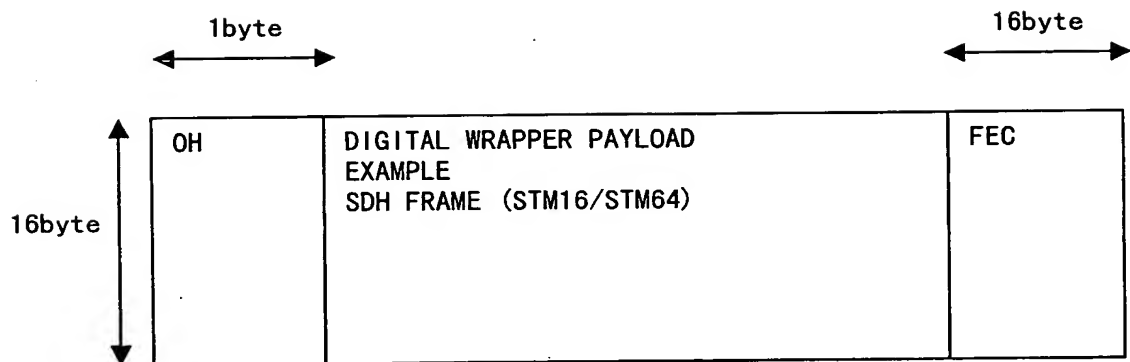


FIG. 1



F I G. 2

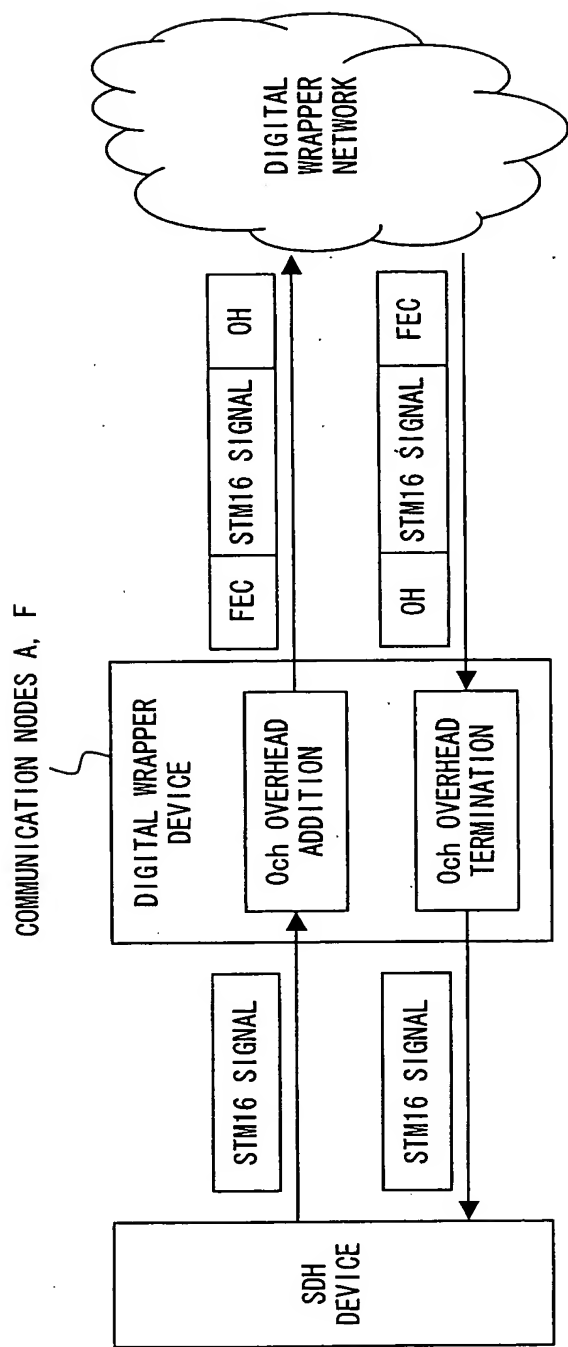


FIG. 3 A

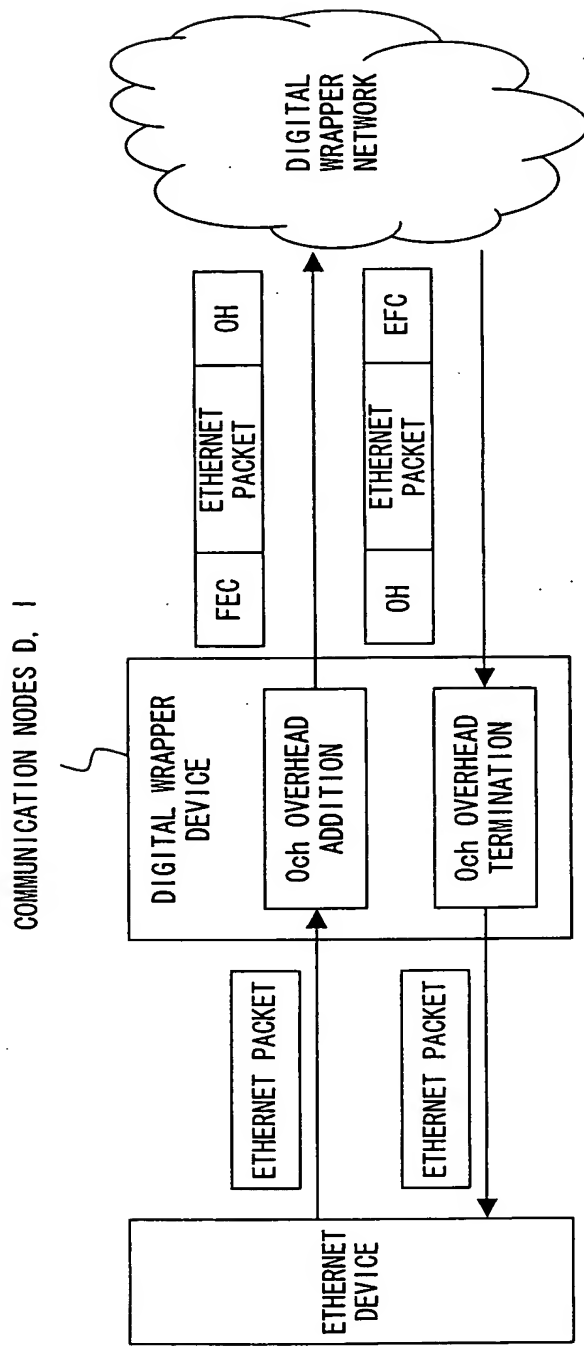


FIG. 3 B

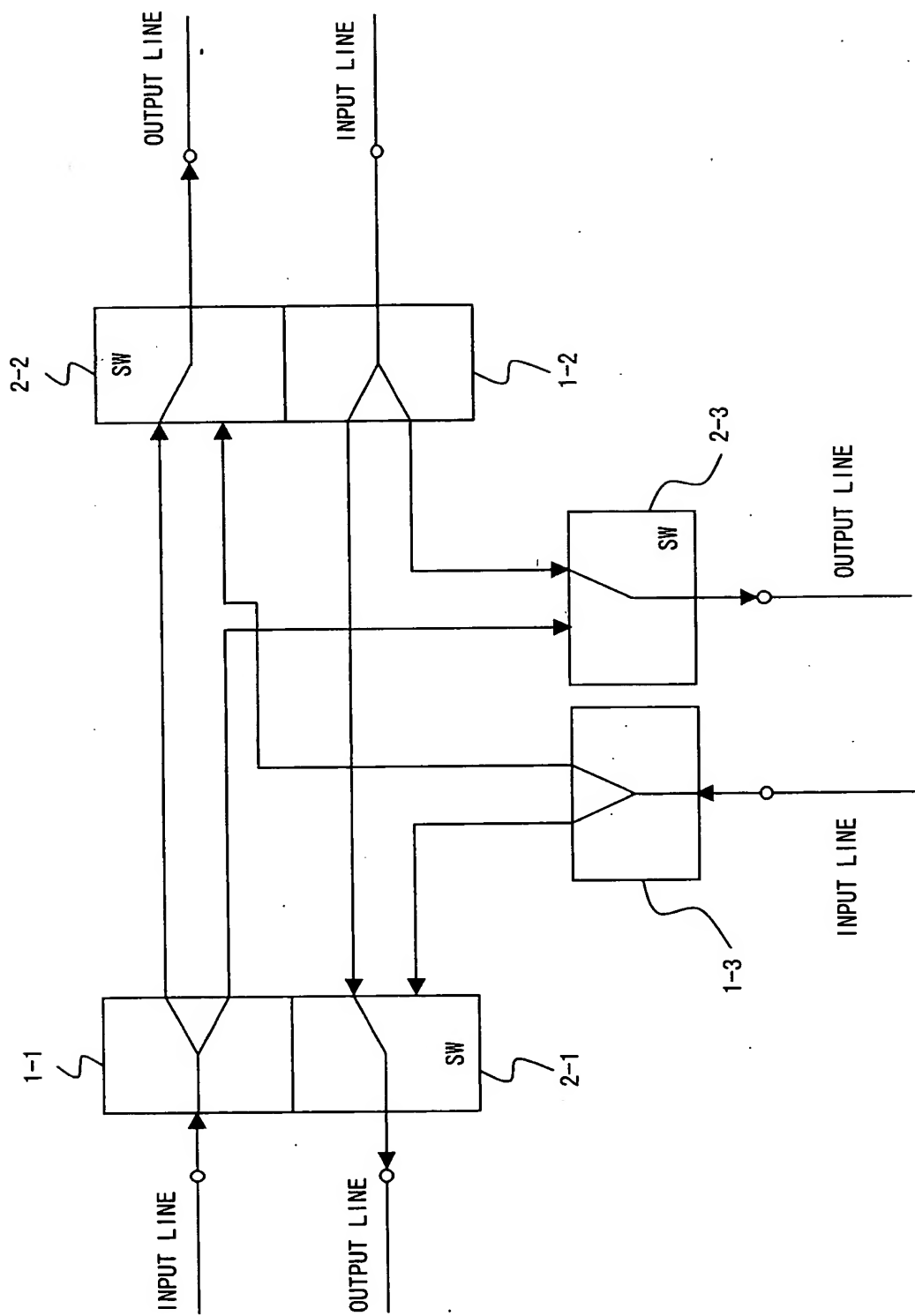


FIG. 4

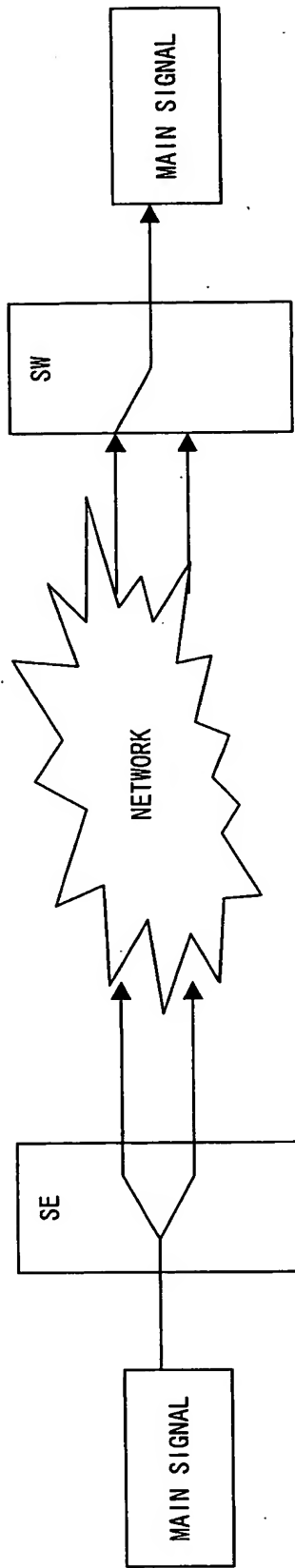


FIG. 5

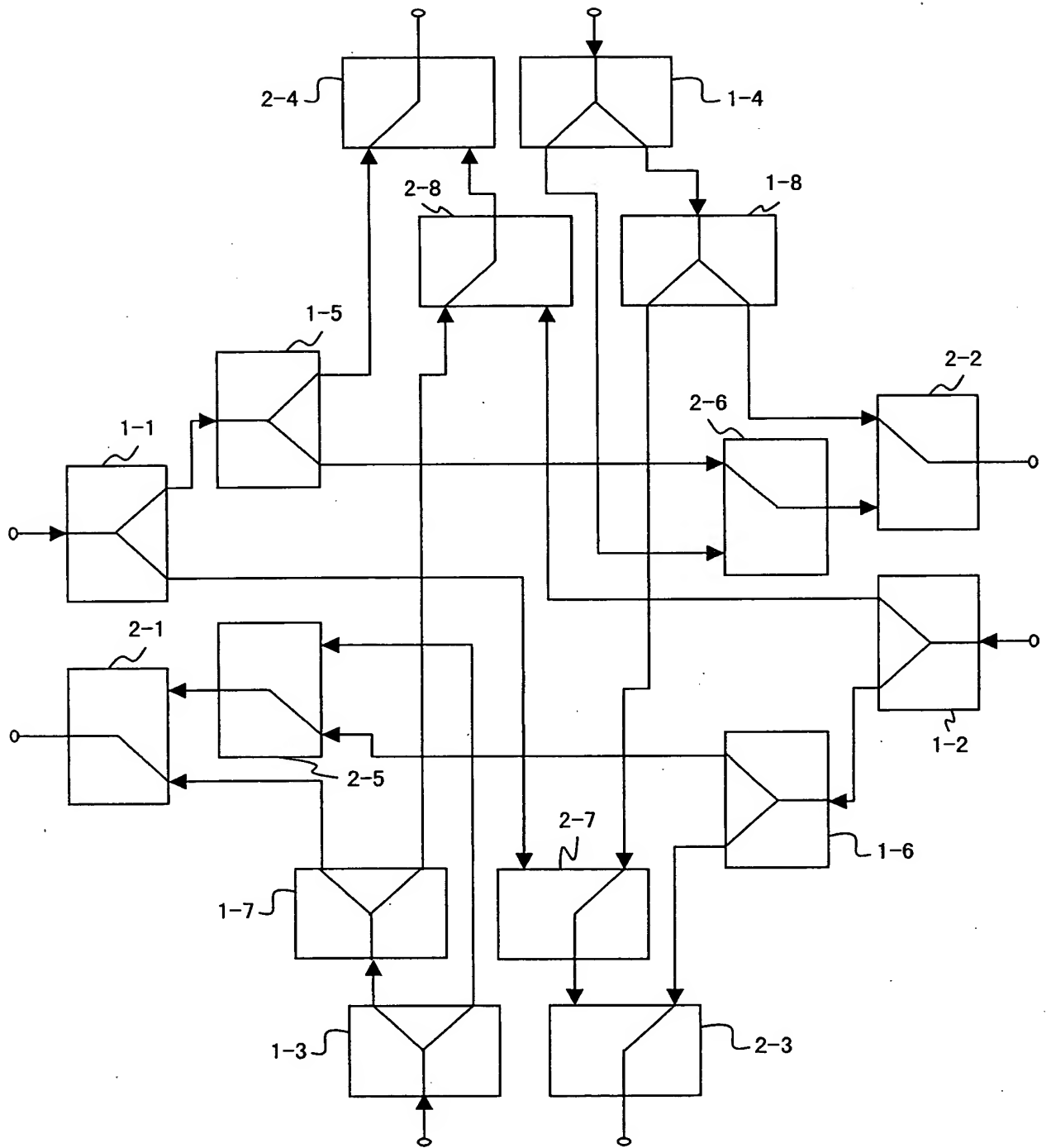


FIG. 6

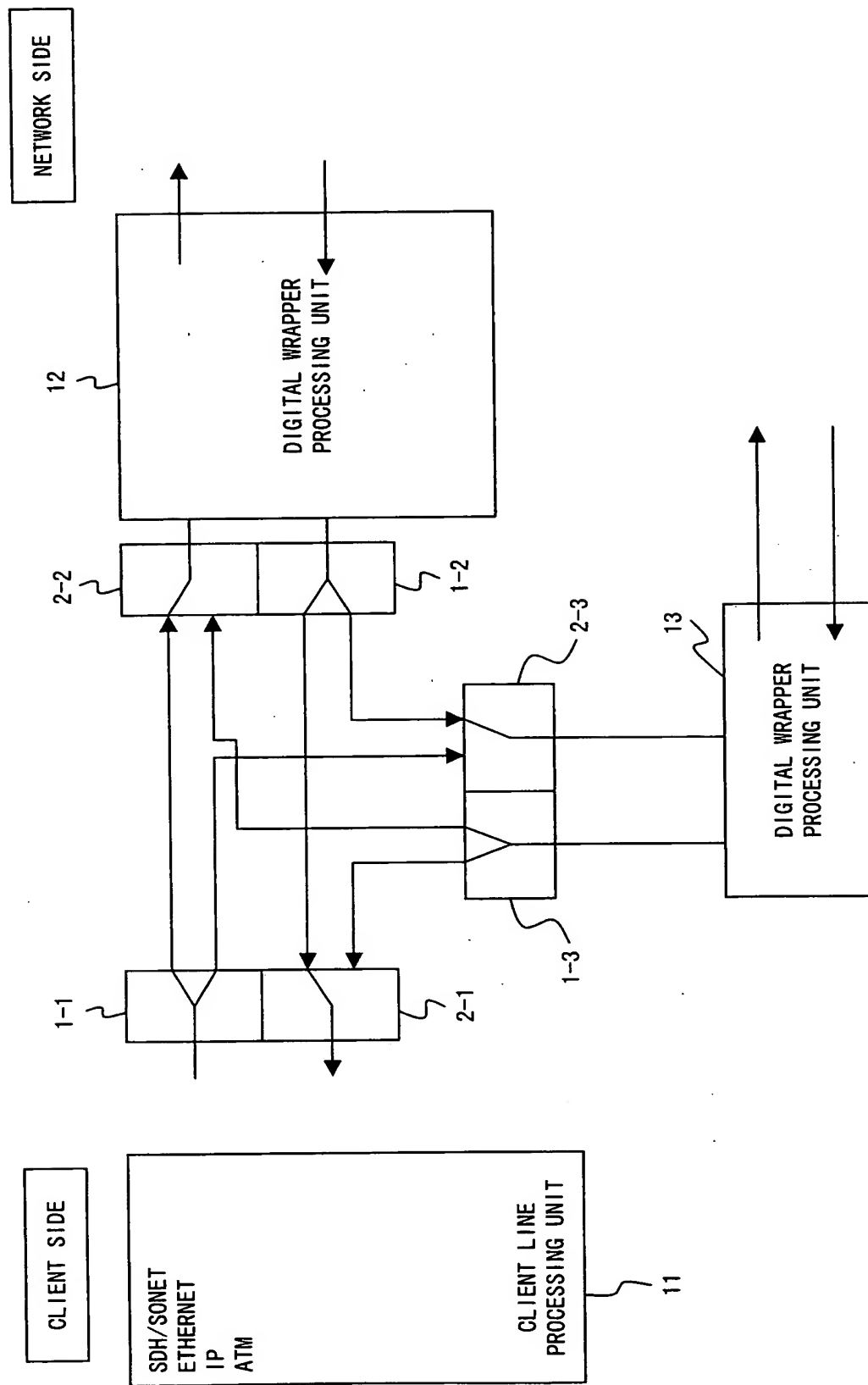


FIG. 7

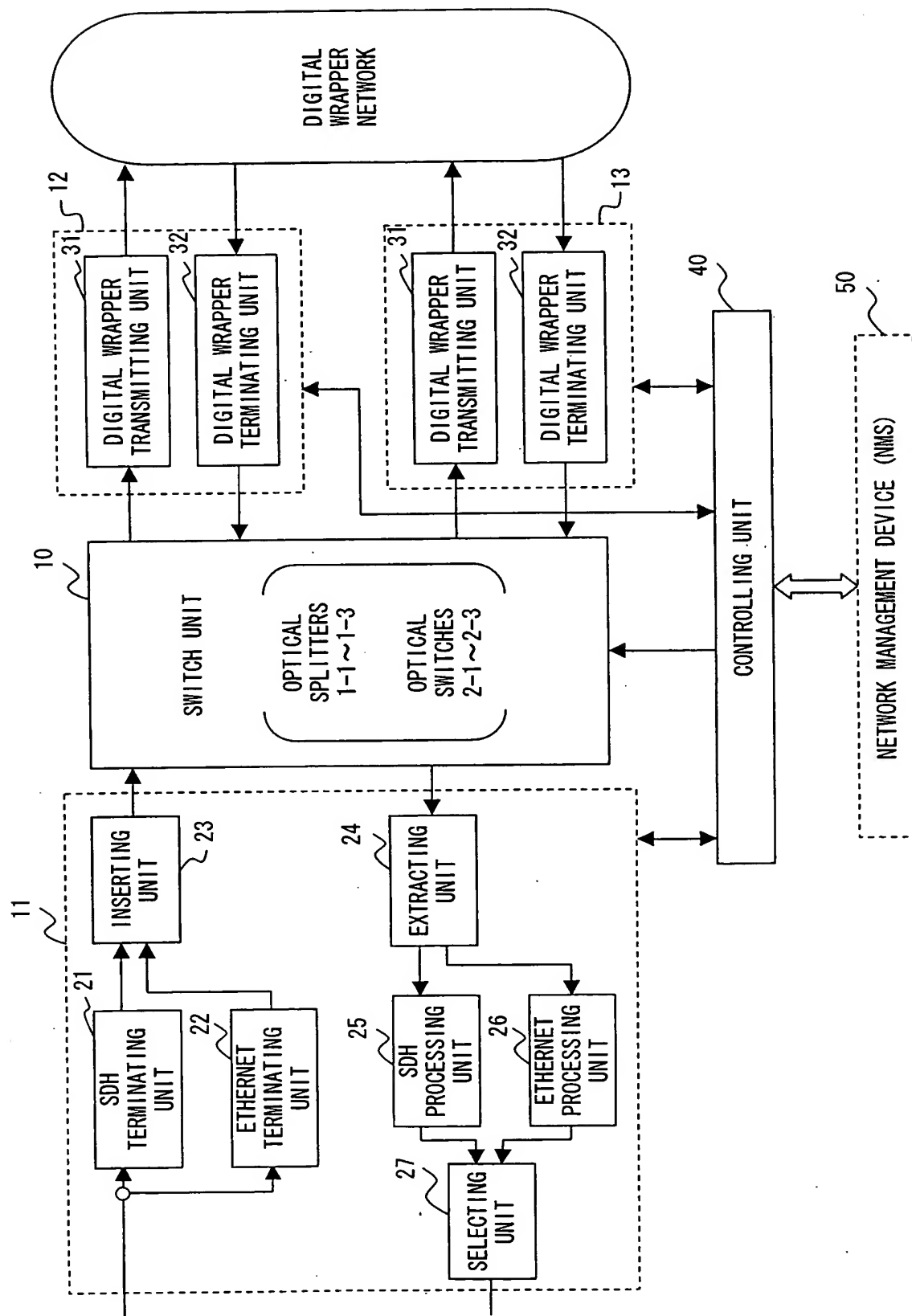


FIG. 8

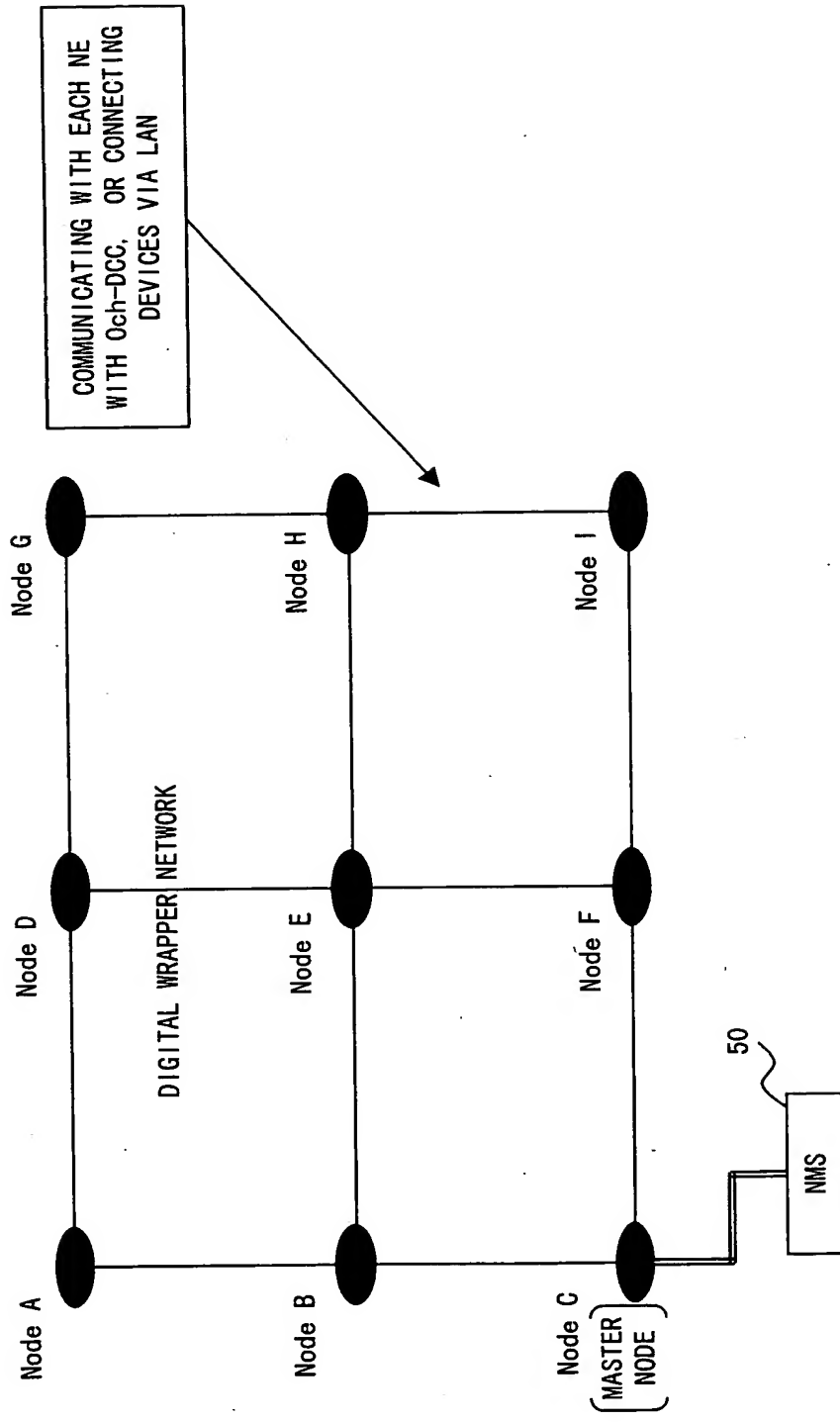


FIG. 9

	A	B	C	D	E	F	G	H	I
A		x		x					
B	x		x		x				
C		x				x			
D	x				x		x		
E		x		x		x		x	
F			x		x				x
G				x				x	
H					x		x		x
I						x		x	

x : LINE EXISTS

FIG. 10

	A	B	C	D	E	F	G	H	I
A		x							
B	x				x				
C									
D							x		
E		x				x			
F					x				
G				x				x	
H							x		x
I								x	

x : IN USE

FIG. 11

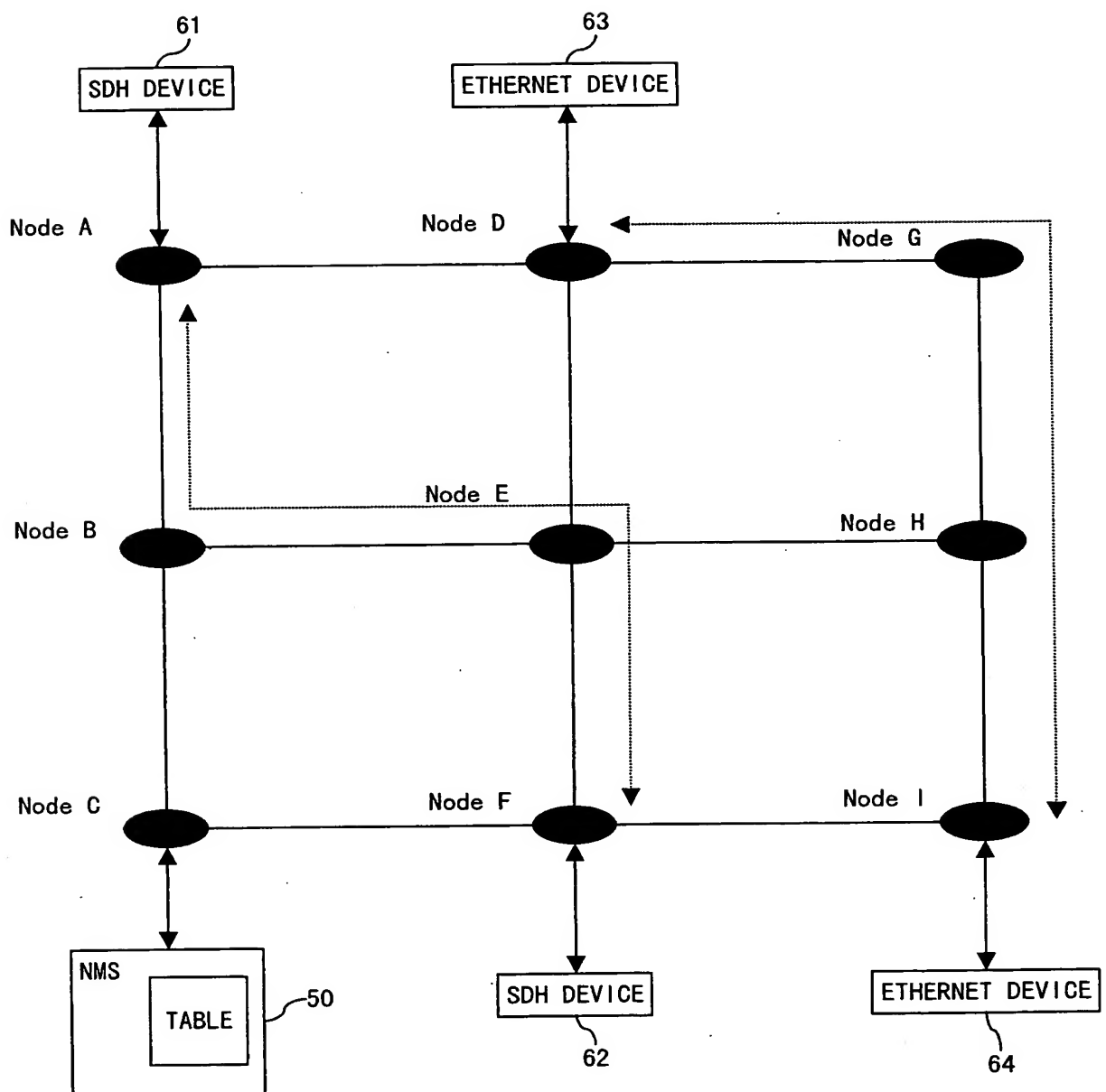


FIG. 12

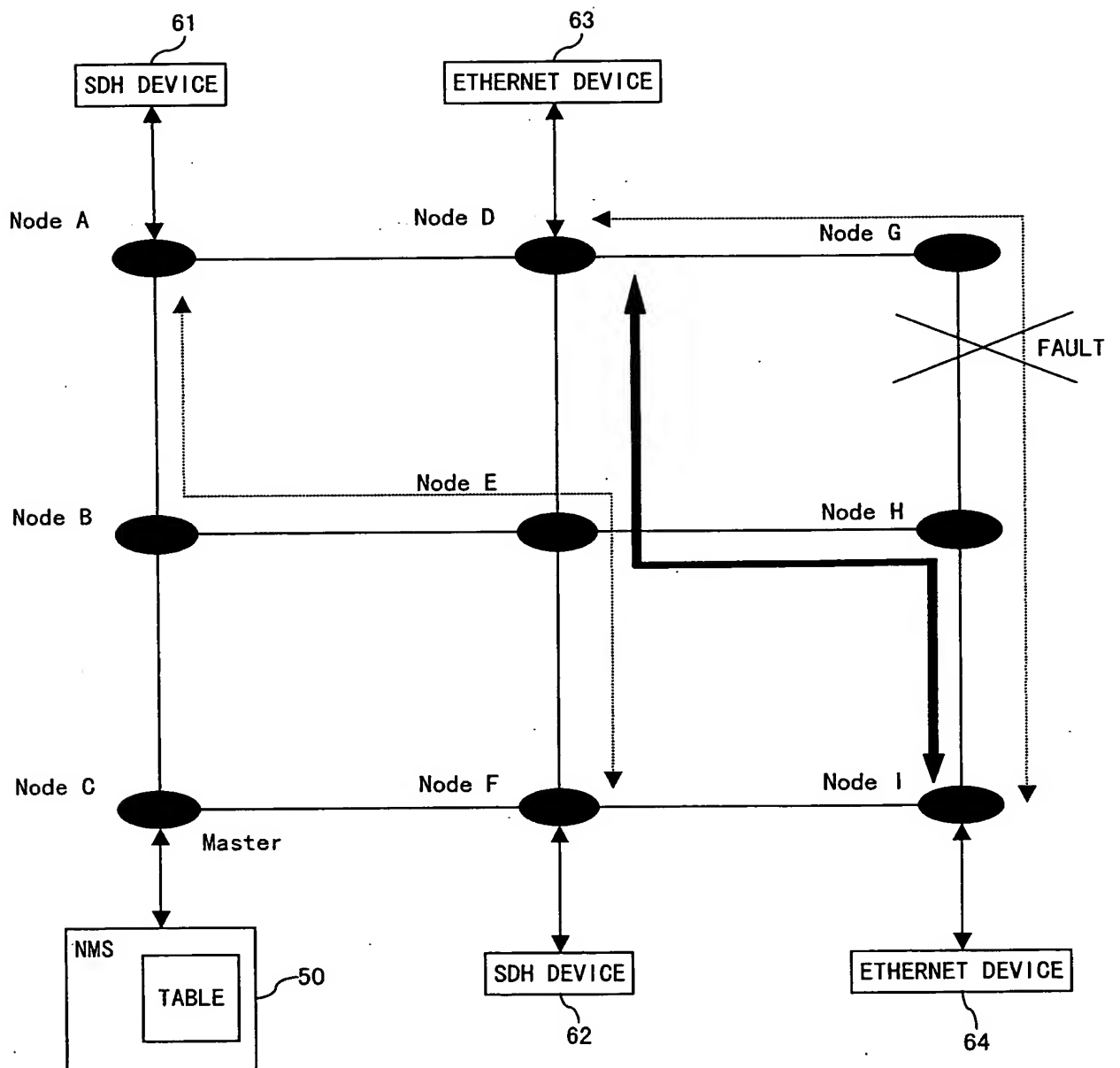


FIG. 13

	A	B	C	D	E	F	G	H	I
A		x							
B	x				x				
C									
D							x		
E		x				x			
F					x				
G				x				x	
H							x		x
I								x	

↓
UPDATE

	A	B	C	D	E	F	G	H	I
A		x							
B	x				x				
C									
D					x				
E		x		x		x		x	
F					x				
G								FAULT	
H					x		FAULT		x
I								x	

x : IN USE

FIG. 14

OH	DIGITAL WRAPPER PAYLOAD (WORKING SYSTEM) SDH FRAME (STM16/STM64)	DIGITAL WRAPPER PAYLOAD (STANDBY SYSTEM)	FEC
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FIG. 15

	A	B	C	D	E	F	G	H	I
A		x							
B	x				x				
C							x		
D									
E		x				x			
F					x				
G				x				x	
H							x		x
I								x	

FIG. 16A

	A	B	C	D	E	F	G	H	I
A		x							
B	x				x				
C									
D					x				
E		x		x		x	x		
F					x	x			x
G								FAULT	
H							FAULT		
I						x			

FIG. 16B

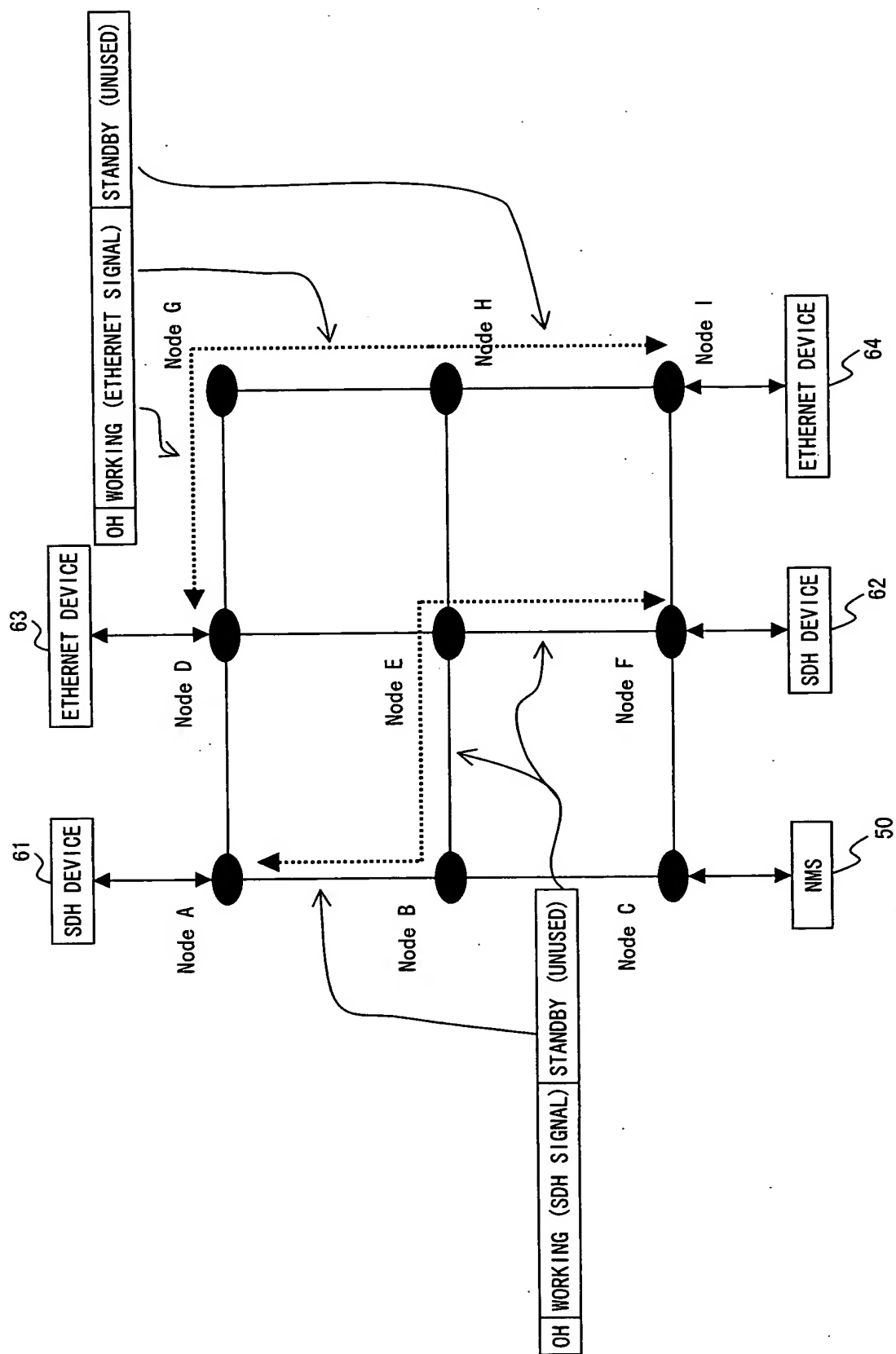


FIG. 17

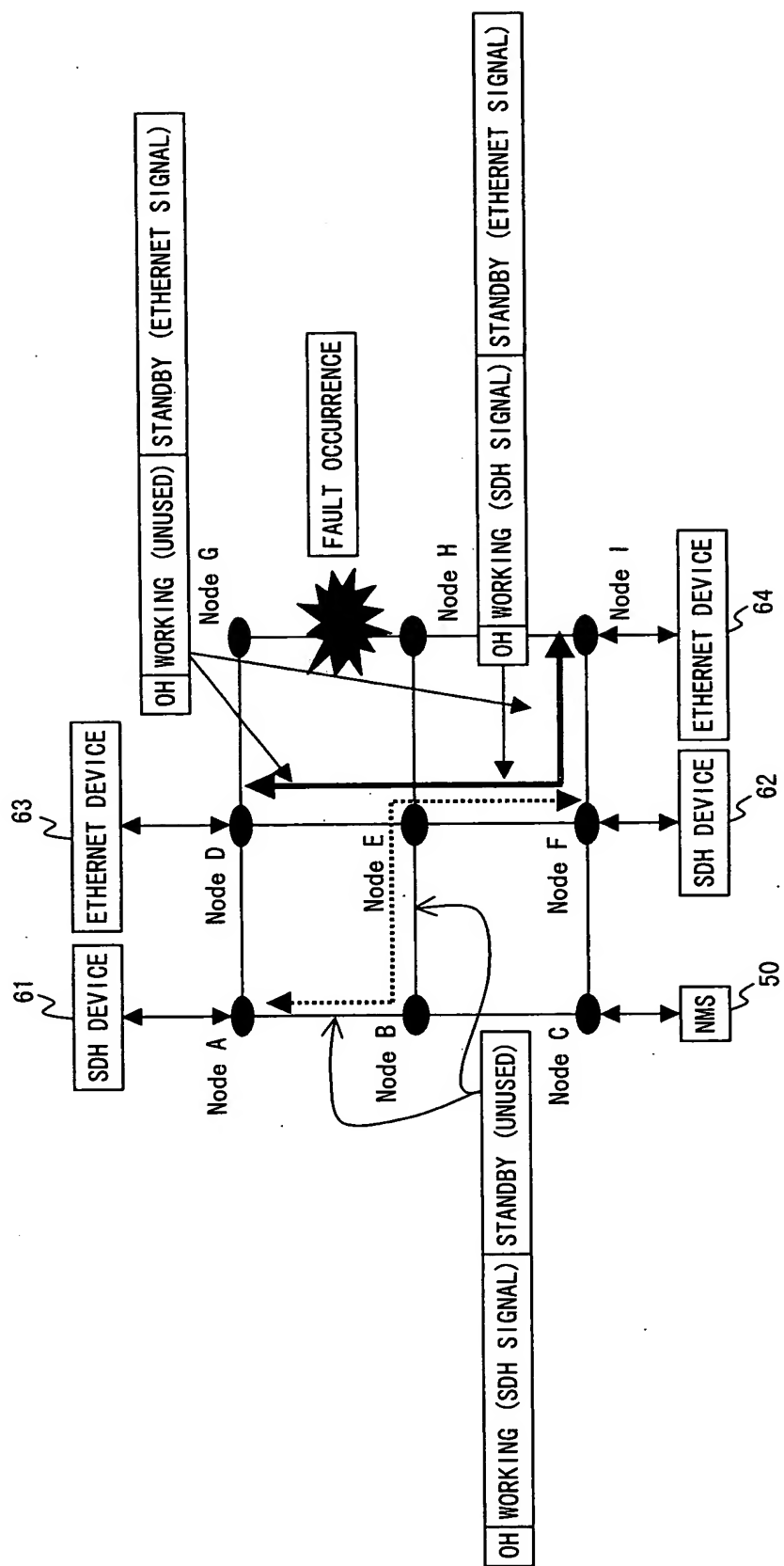


FIG. 18

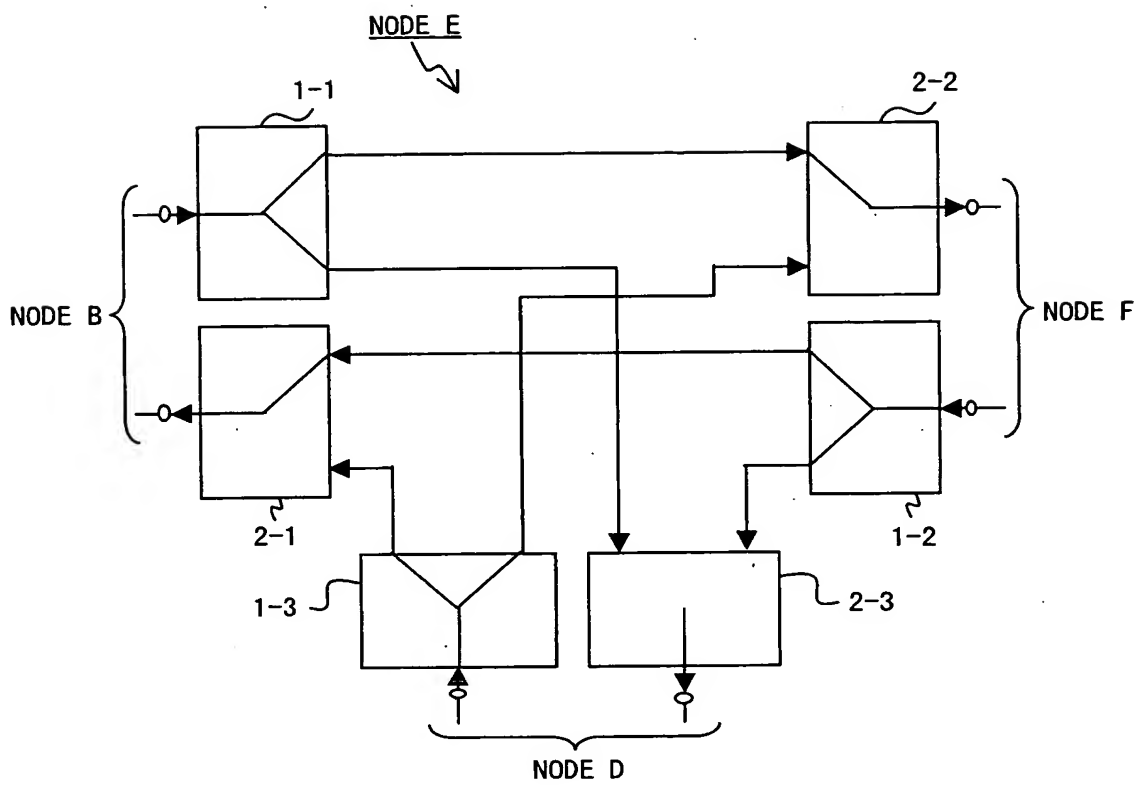


FIG. 19A

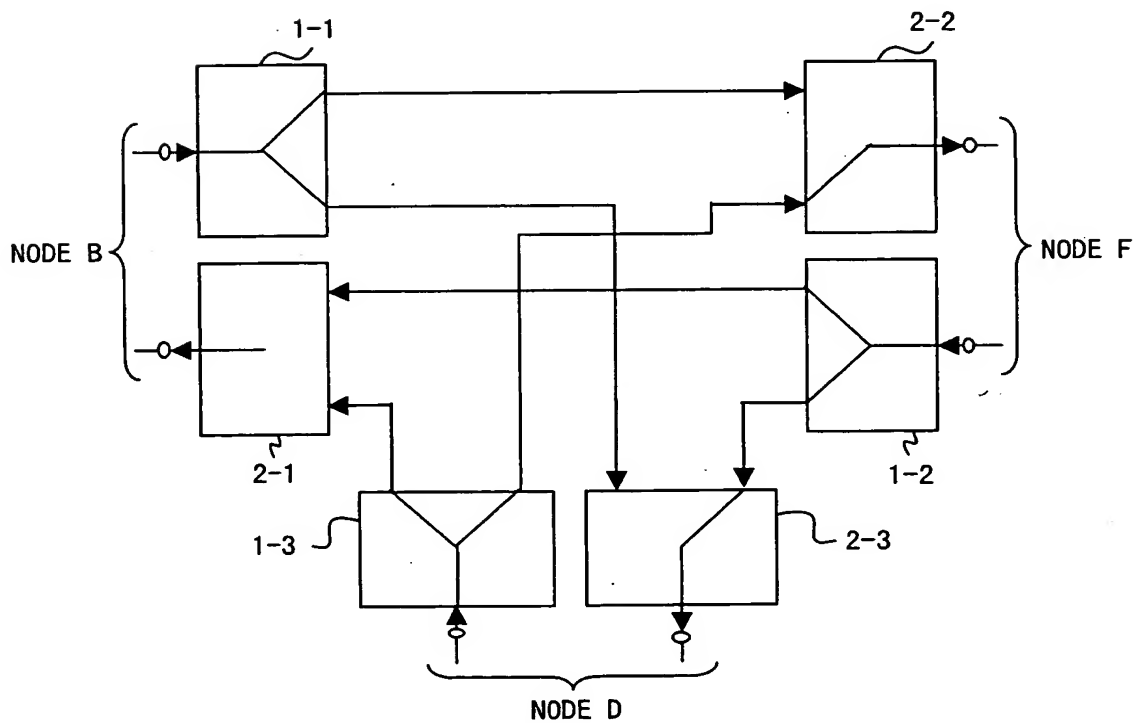


FIG. 19B

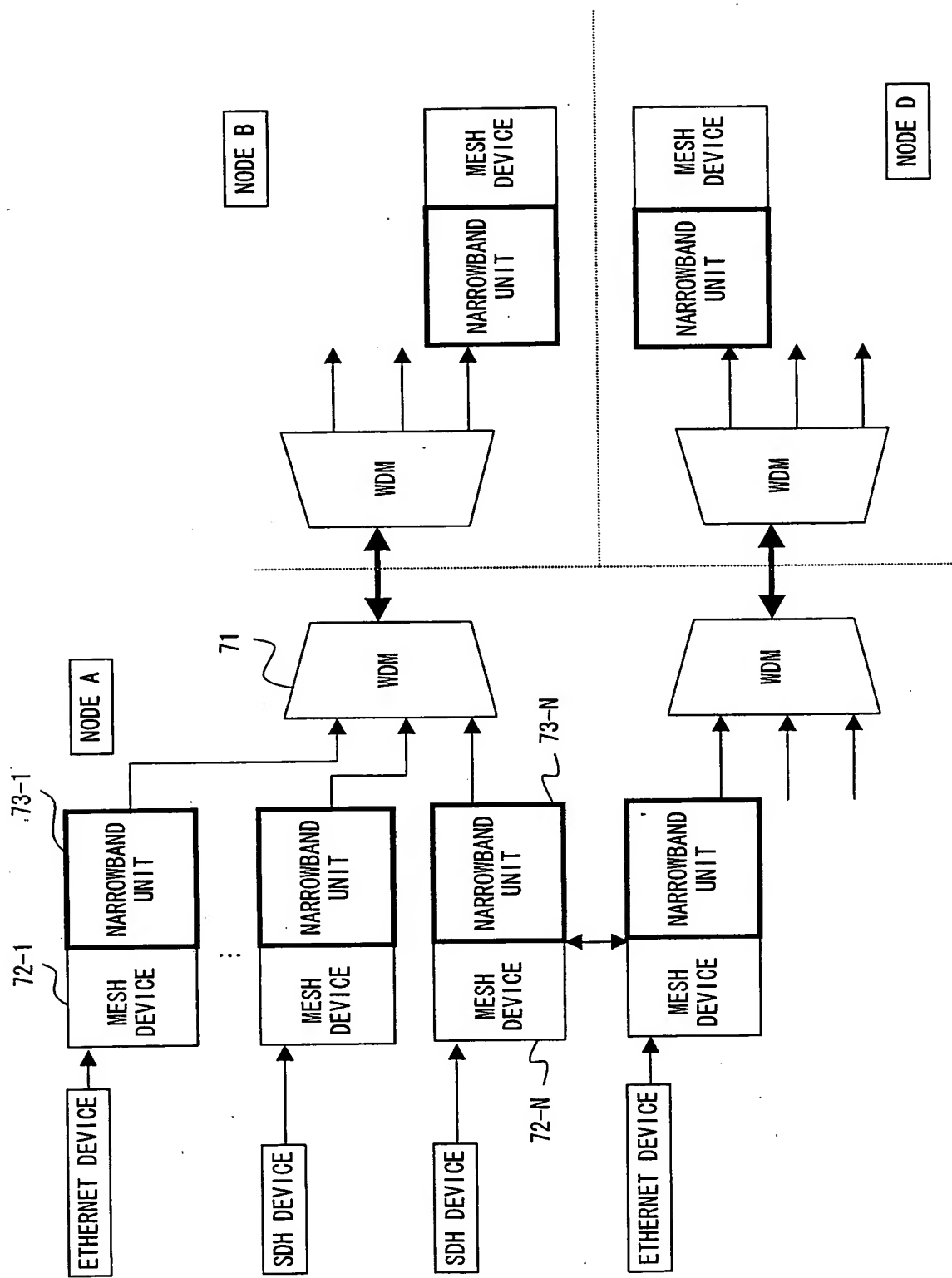


FIG. 20

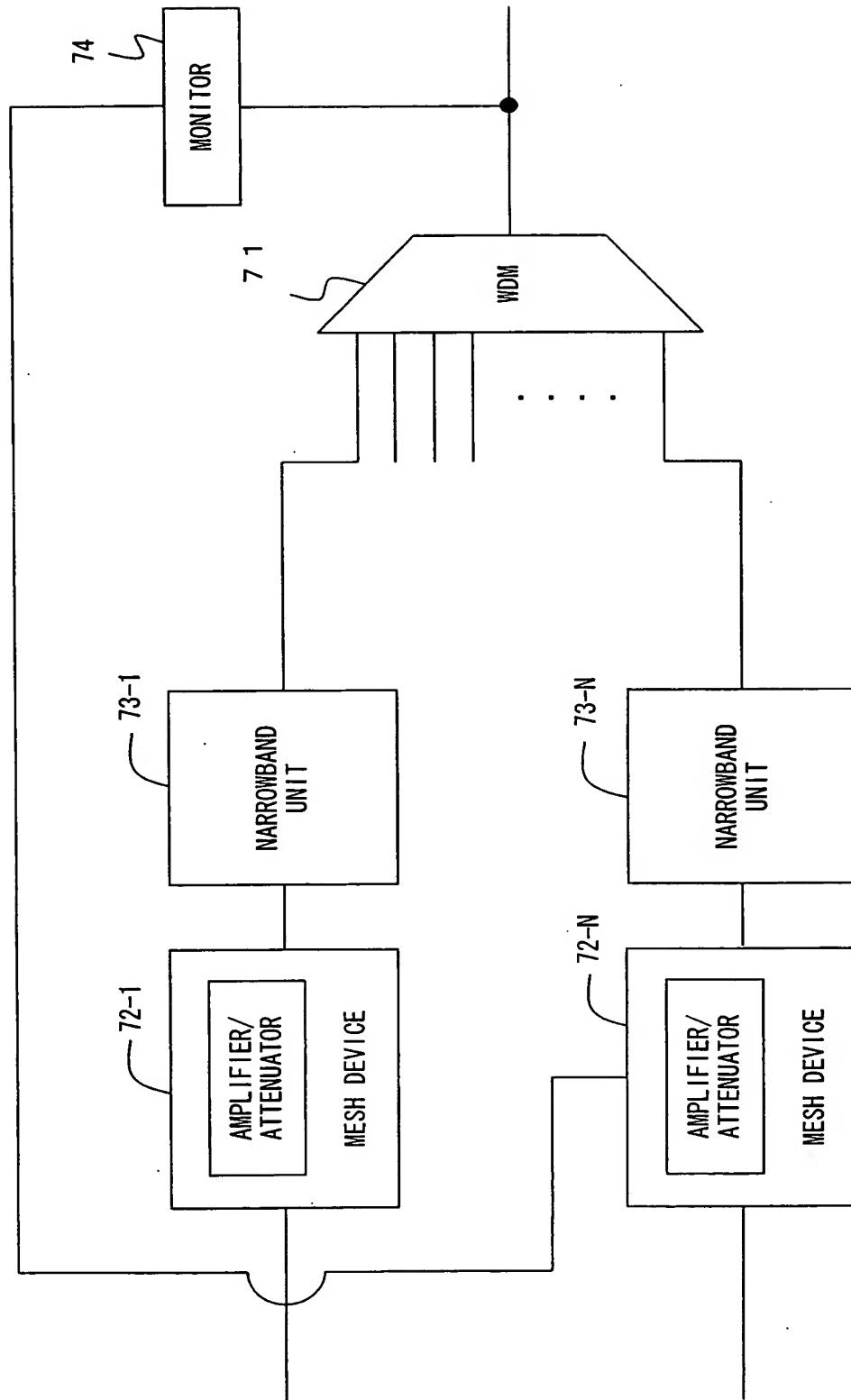


FIG. 21

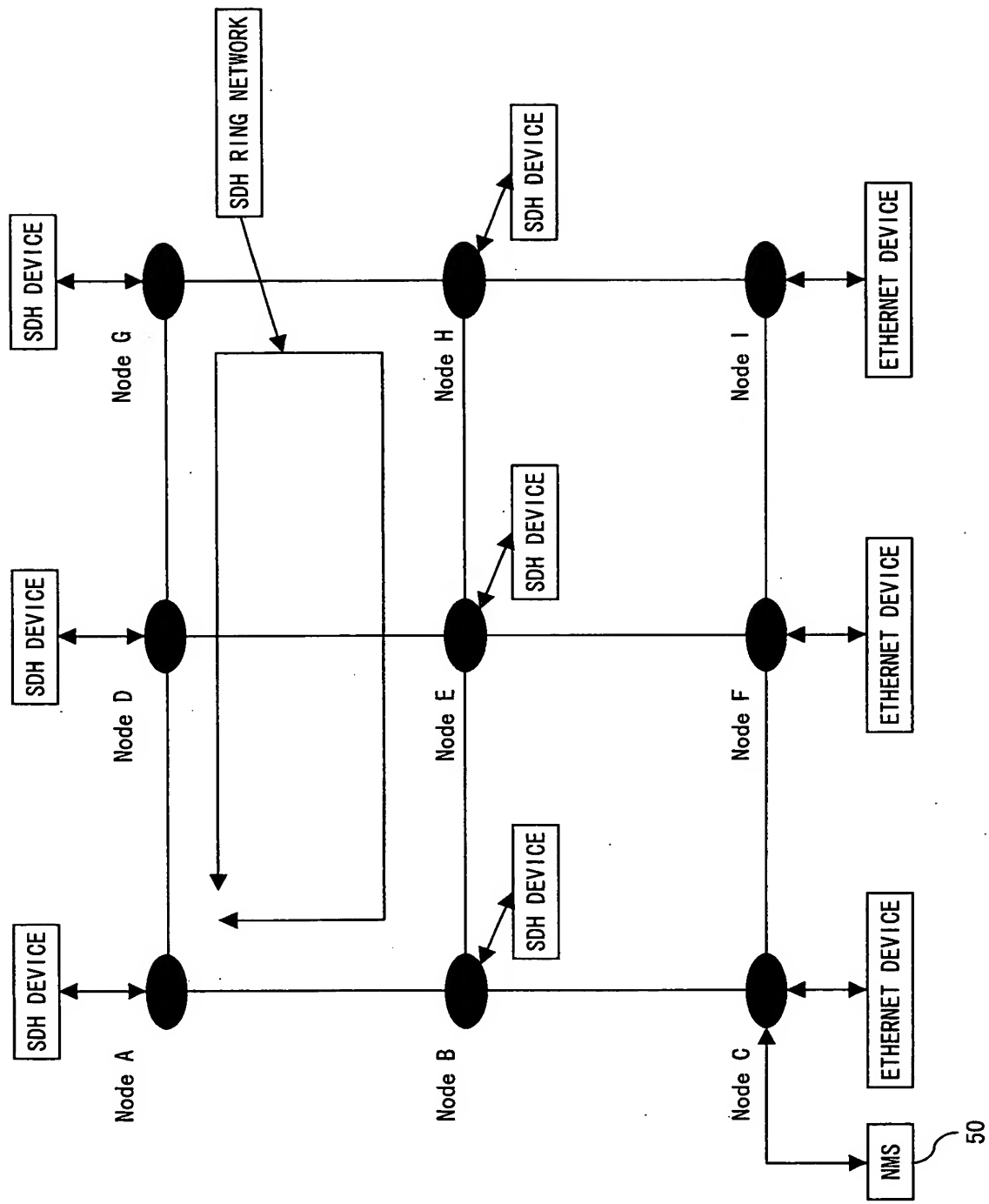


FIG. 22

	A	B	C	D	E	F	G	H	I
A		SDH		SDH					
B	SDH				SDH				
C						ETHERNET			
D	SDH						SDH		
E		SDH						SDH	
F			ETHERNET						ETHERNET
G				SDH				SDH	
H					SDH		SDH		
I						ETHERNET			

FIG. 23

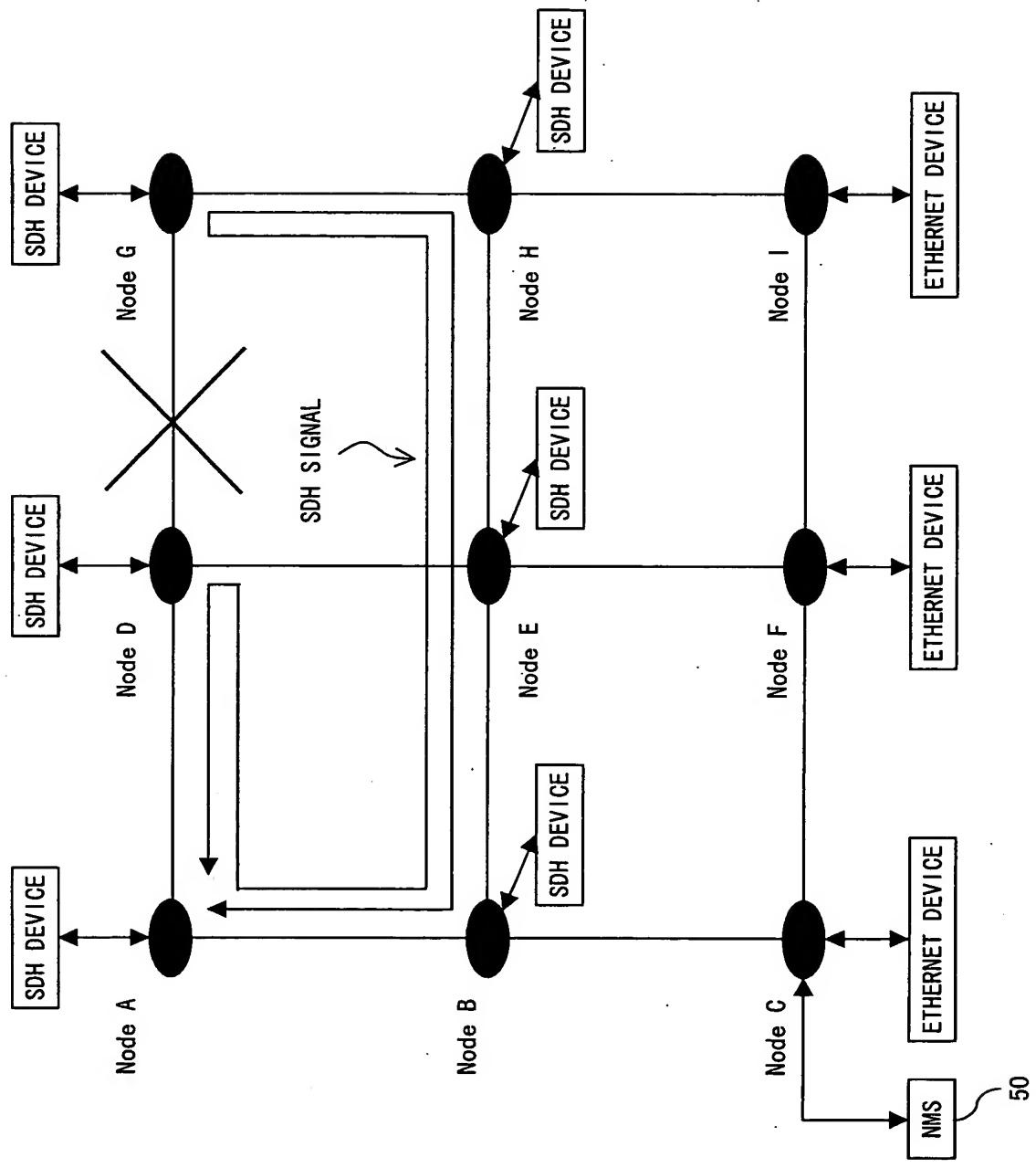


FIG. 24

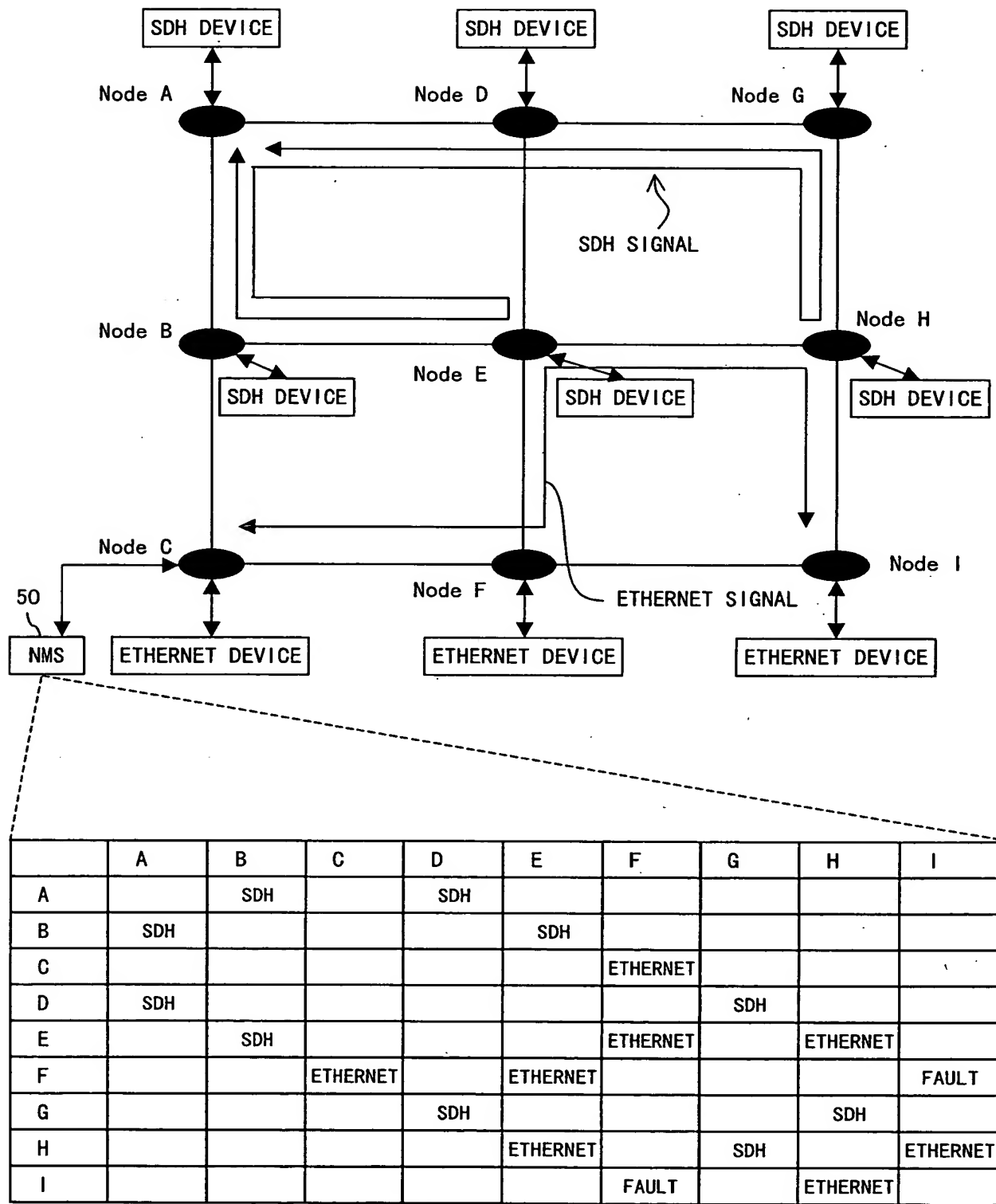


FIG. 25

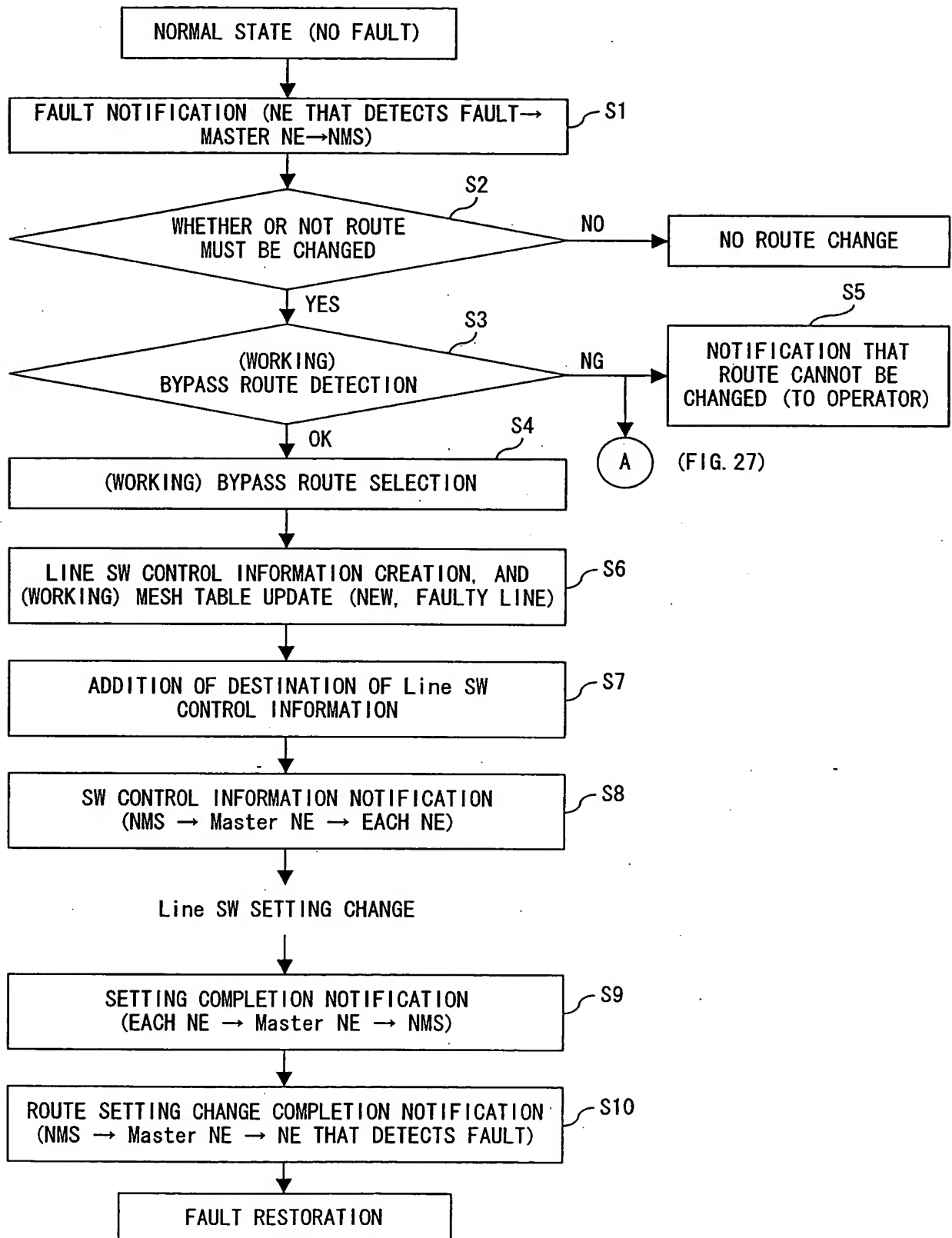


FIG. 26

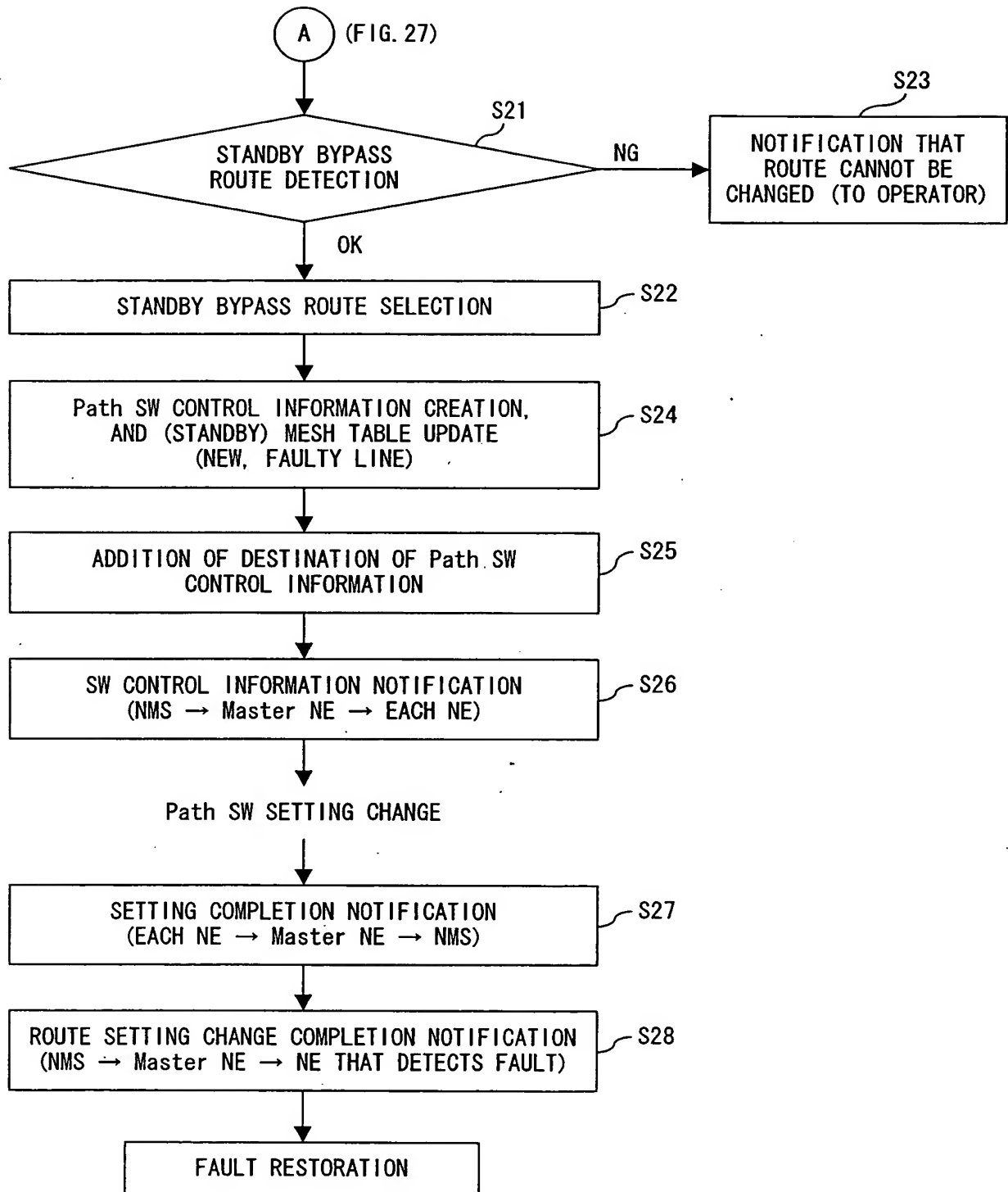


FIG. 27